

**REMARKS**

Claims 1-34 are pending in this application.

By this Amendment, independent claims 1, 2, 13 and 14 are amended, and dependent claims 31-34 are added to recite additional features disclosed in the specification at, for example, Fig. 2, and paragraphs [0011], [0040], [0075], [0081], [0090] and [0114].

Reconsideration of the Application is respectfully requested.

The Office Action rejects claims 1-30 under 35 U.S.C. §103(a) over Applicant's Admitted Prior Art (AAPA) in view of U.S. Patent No. 6,362,798 to Kimura et al. This rejection is respectfully traversed.

The Office Action admits that AAPA does not disclose or suggest a transforming circuit using a threshold voltage of a transistor that is substantially identical to a threshold voltage of one of a plurality of current-generating active elements. However, the Office Action asserts that Kimura discloses such a transforming circuit 120, as shown in Fig. 1 and described at col. 10, lines 19-25 of Kimura.

Independent claims 1, 2, 13 and 14 are amended to recite additional features, as outlined above. AAPA and Kimura do not disclose or suggest the subject matter recited in the amended claims.

Kimura discloses a compensating TFT 120 that changes a voltage  $V_{sig}$  to voltage  $V_g$ , such that  $V_g$ , instead of  $V_{sig}$ , is supplied to the gate of driving TFT 110. See Fig. 1. Kimura discloses setting the threshold characteristics of the driving TFT 110 and the compensating TFT 120 close to each other to offset the threshold voltages of the driving TFT 110 and the compensating TFT 120. See col. 9, lines 54-67, and col. 10, lines 19-25.

In view of the above, Kimura only discloses supplying the output of compensating TFT 122 the gate of a single driving TFT 110. Kimura does not disclose or suggest supplying the output of compensating TFT 120 to a plurality of gates or control terminals of a plurality

of current-generating active elements, as recited in claim 1, and similarly recited in claims 2, 13 and 14.

Furthermore, Kimura only discloses that the compensating TFT 120 is connected to the driving TFT 110. Kimura does not disclose or suggest that the compensating TFT 120 is disposed in physical proximity to the driving TFT 110. In fact, because Kimura does not disclose or suggest supplying the output of compensating TFT 120 to the gates or control terminals of a plurality of current-generating active elements, as discussed above, Kimura does not disclose or suggest a voltage-rising transistor that is located in physical proximity to the plurality of current-generating active elements, as recited in claim 1, and similarly recited in claims 2, 13 and 14. Also, AAPA does not disclose or suggest these features.

For at least the above reasons, AAPA and Kimura, either individually or in combination, do not disclose or suggest the subject matter recited in claims 1, 2, 13 and 14, and claims 3-12 and 15-30 depending therefrom. Accordingly, withdrawal of the rejection of claims 1-30 under 35 U.S.C. §103(a) is respectfully requested.

Claims 31-34 are patentable at least in view of the patentability of claims 1, 2, 13 and 14, from which they respectively depend, as well as for additional features they recite.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 1-34 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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Attachment:  
Amendment Transmittal

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